

## **PC354N Series**

# Mini-flat Package, AC Input Photocoupler



#### ■ Description

**PC354N Series** contains an IRED optically coupled to a phototransistor.

It is packaged in a 4-pin Mini-flat package. Input-output isolation voltage(rms) is 3.75kV. Collector-emitter voltage is 80V(\*) and CTR is 20% to 400% at input current of ±1mA.

#### ■ Features

- 1. 4-pin Mini-flat package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. AC input type
- 4. High collector-emitter voltage (V<sub>CEO</sub>: 80V<sup>(\*)</sup>)
- 5. High isolation voltage between input and output  $(V_{iso(rms)}: 3.75kV)$ 
  - (\*) Up to Date code "P9" (September 2002)  $V_{CEO}$ : 35V. From the production Date code "J5" (May 1997) to "P9" (September 2002), however the products were screened by  $BV_{CEO} \ge 70V$ .

#### ■ Agency approvals/Compliance

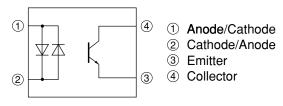
- 1. Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. **PC354**)
- 2. Package resin : UL flammability grade (94V-0)

#### ■ Applications

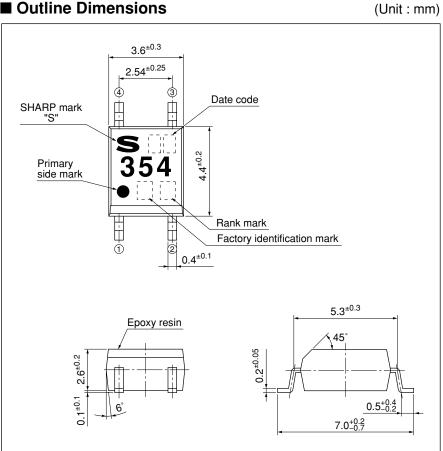
- 1. Hybrid substrates that require high density mounting.
- 2. Programmable controllers



## ■ Internal Connection Diagram



## **■** Outline Dimensions



Product mass: approx. 0.1g



## Date code (2 digit)

	1st o	digit		2nd digit		
	Year of p	roduction		Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

## Factory identification mark

Factory identification Mark	Country of origin	
no mark	Taman	
	Japan	
	Indonesia	
$\overline{\hspace{1cm}}$	Philippines	
_	China	

<sup>\*</sup> This factory marking is for identification purpose only.

Please contact the local SHARP sales representative to see the actual status of the production.

## Rank mark

Refer to the Model Line-up table



**■** Absolute Maximum Ratings

 $(T_a=25^{\circ}C)$ 

			9	$(1_{a}-25\mathbf{C})$
	Parameter	Symbol	Rating	Unit
t	Forward current	$I_{\mathrm{F}}$	±50	mA
Input	*1 Peak forward current	$I_{FM}$	±1	A
<u> </u>	Power dissipation	P	70	mW
	Collector-emitter voltage	$V_{CEO}$	*4 80	V
Output	Emitter-collector voltage	V <sub>ECO</sub>	6	V
Out	Collector current	$I_{C}$	50	mA
	Collector power dissipation	$P_{C}$	150	mW
	Total power dissipation	P <sub>tot</sub>	170	mW
Operating temperature		$T_{opr}$	-30 to +100	°C
Storage temperature		T <sub>stg</sub>	-40 to +125	°C
*2]	Isolation voltage	V <sub>iso (rms)</sub>	3.75	kV
*3 (	Soldering temperature	$T_{sol}$	260	°C

<sup>\*1</sup> Pulse width≤100µs, Duty ratio: 0.001 \*2 40 to 60%RH, AC for 1 minute, f=60Hz

## **■** Electro-optical Characteristics

 $(T_a=25^{\circ}C)$ 

	Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Immust	Forward voltage		$V_F$	I <sub>F</sub> =±20mA	_	1.2	1.4	V
Input	Terminal capa	acitance	$C_{t}$	V=0, f=1kHz	-	30	250	pF
	Collector dark	current	$I_{CEO}$	$V_{CE}$ =50V, $I_F$ =0	_	_	100	nA
Output	Collector-emitter brea	Collector-emitter breakdown voltage		$I_{C}=0.1 \text{mA}, I_{F}=0$	*5 80	-	-	V
	Emitter-collector breakdown voltage		$BV_{ECO}$	$I_{E}=10\mu A, I_{F}=0$	6	-	-	V
	Collector curr	Collector current		$I_F=\pm 1$ mA, $V_{CE}=5$ V	0.2	_	4.0	mA
		ollector-emitter saturation voltage		$I_F=\pm 20mA$ , $I_C=1mA$	_	0.1	0.2	V
Transfer	Isolation resis	stance	$R_{ISO}$	DC500V, 40 to 60%RH	5×10 <sup>10</sup>	1×10 <sup>11</sup>	-	Ω
charac- teristics	Floating capacitance		$C_{\mathrm{f}}$	V=0, $f=1MHz$	_	0.6	1.0	pF
corrector	Daspansa tima	Rise time	$t_r$	$V_{CF}=2V$ , $I_C=2mA$ , $R_I=100\Omega$	_	4	18	μs
	Kesponse unic	esponse time Fall time $t_f$		V CE=2 V, IC=2IIIA, RL=100\$2	_	3	18	μs

<sup>\*5</sup> From the production Date code "J5" (May 1997) to "P9" (September 2002), however the products were screened by BV<sub>CEO</sub>≥70V.

<sup>\*3</sup> For 10s \*4 Up to Date code "P9" (September 2002) V<sub>CEO</sub>: 35V.



## **■** Model Line-up

Package	Тар	ing	Rank mark	I <sub>C</sub> [mA]	
	3 000 pcs/reel	750 pcs/reel	Kalik iliaik	$(I_F=\pm 1 \text{mA}, V_{CE}=5 \text{V}, T_a=25 ^{\circ}\text{C})$	
Model No.	PC354N	PC354NT	A or no mark	0.2 to 4.0	
	PC354N1	PC354N1T	A	0.5 to 1.5	

Please contact a local SHARP sales representative to inquire about production status and Lead-Free options.

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Fig.1 Forward Current vs. Ambient Temperature

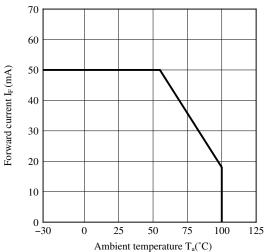


Fig.3 Collector Power Dissipation vs. Ambient Temperature

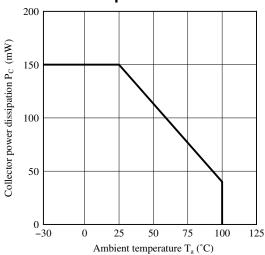


Fig.5 Peak Forward Current vs. Duty Ratio

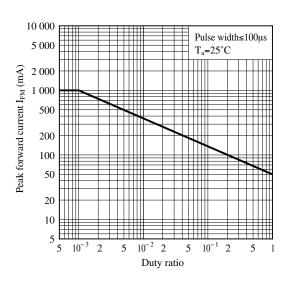


Fig.2 Diode Power Dissipation vs. Ambient Temperature

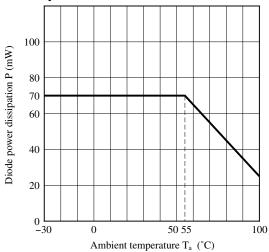


Fig.4 Total Power Dissipation vs. Ambient Temperature

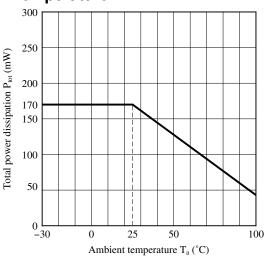


Fig.6 Forward Current vs. Forward Voltage

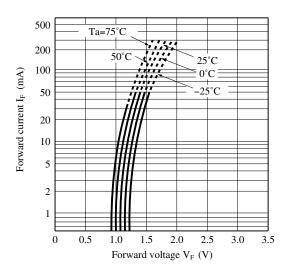




Fig.7 Current Transfer Ratio vs. Forward Current

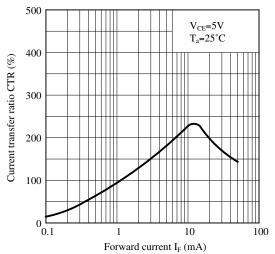


Fig.9 Relative Current Transfer Ratio vs. Ambient Temperature

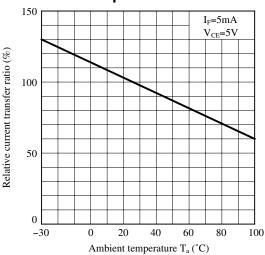


Fig.11 Collector Dark Current vs. Ambient Temperature

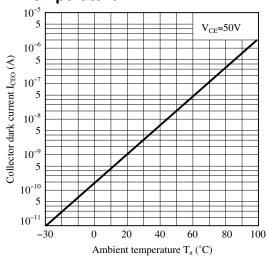


Fig.8 Collector Current vs. Collector-emitter Voltage

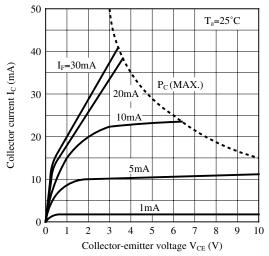


Fig.10 Collector - emitter Saturation Voltage vs. Ambient Temperature

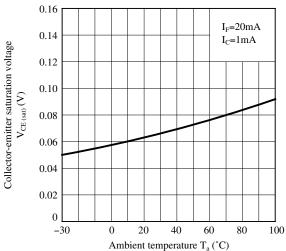


Fig.12 Response Time vs. Load Resistance

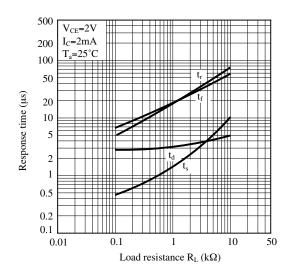
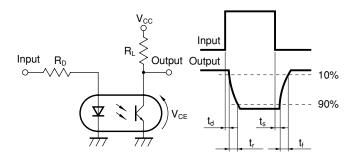


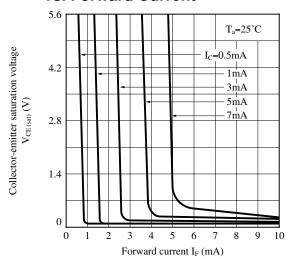


Fig.13 Test Circuit for Response Time



Please refer to the conditions in Fig.12

Fig.14 Collector-emitter Saturation Voltage vs. Forward Current



Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



## ■ Design Considerations

## Design guide

While operating at I<sub>F</sub><1.0mA, CTR variation may increase.

Please make design considering this fact.

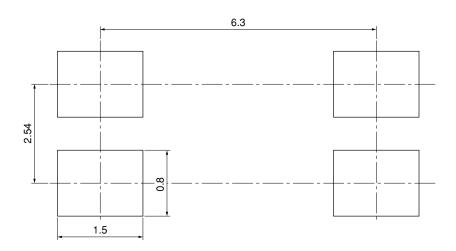
This product is not designed against irradiation and incorporates non-coherent IRED.

## Degradation

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5years) into the design consideration.

## Recommended Foot Print (reference)



(Unit:mm)

<sup>☆</sup> For additional design assistance, please review our corresponding Optoelectronic Application Notes.



## ■ Manufacturing Guidelines

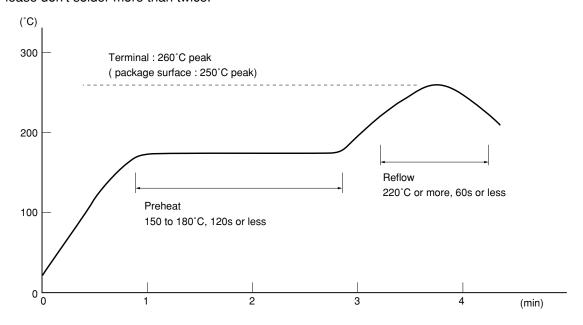
## Soldering Method

#### Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



#### Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 260°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

#### Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

#### Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



#### Cleaning instructions

#### Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

#### Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

#### Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

#### Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

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## ■ Package specification

## ● Tape and Reel package

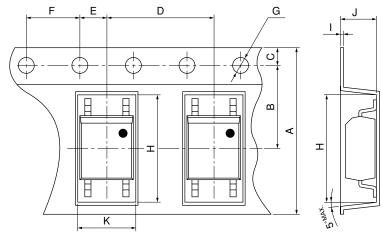
1. 3 000pcs/reel Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

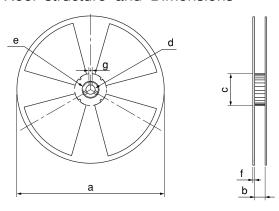
Reel: PS

## Carrier tape structure and Dimensions



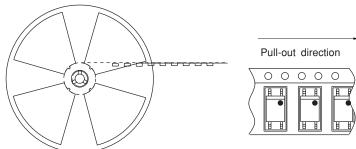
Dimensions List (Unit: mm)						
A	В	С	D	Е	F	G
12.0±0.3	5.5 <sup>±0.1</sup>	1.75 <sup>±0.1</sup>	8.0 <sup>±0.1</sup>	2.0 <sup>±0.1</sup>	4.0 <sup>±0.1</sup>	φ1.5 <del>+</del> 8.1
Н	I	J	K			
7.4 <sup>±0.1</sup>	0.3±0.05	3.1 <sup>±0.1</sup>	4.0 <sup>±0.1</sup>			

## Reel structure and Dimensions



Dimensio	ns List	(Unit: mm)		
a	b	С	d	
370	13.5 <sup>±1.5</sup>	80 <sup>±1.0</sup>	13 <sup>±0.5</sup>	
e	f	g		
21 <sup>±1.0</sup>	2.0 <sup>±0.5</sup>	2.0 <sup>±0.5</sup>		

## Direction of product insertion



[Packing: 3 000pcs/reel]



## 2. 750pcs/reel

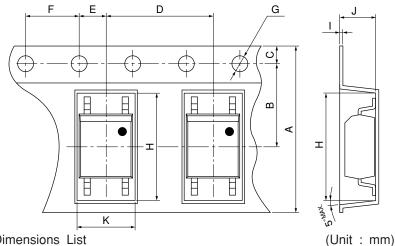
## Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

Reel: PS

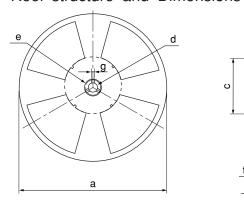
## Carrier tape structure and Dimensions



**Dimensions List** 

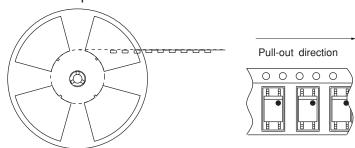
Emigración Liet (Omit :						,,,,,,
A	В	С	D	Е	F	G
12.0±0.3	5.5 <sup>±0.1</sup>	1.75 <sup>±0.1</sup>	8.0 <sup>±0.1</sup>	2.0 <sup>±0.1</sup>	4.0 <sup>±0.1</sup>	φ1.5 <sup>+0.1</sup>
Н	I	J	K			
7.4 <sup>±0.1</sup>	0.3 <sup>±0.05</sup>	3.1 <sup>±0.1</sup>	4.0 <sup>±0.1</sup>			

## Reel structure and Dimensions



Dimensio	ns List	(Unit: mm)		
a	b	С	d	
180	13.5 <sup>±1.5</sup>	80 <sup>±1.0</sup>	13 <sup>±0.5</sup>	
e	f	g		
21 <sup>±1.0</sup>	2.0±0.5	2.0±0.5		

## Direction of product insertion



[Packing: 750pcs/reel]



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  - --- Consumer electronics
- (ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection

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- --- Alarm equipment
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